

ABSTRACT OF THE DISCLOSURE

Multiple memory devices can be managed as though they were one memory device. A memory device that has a logical memory address map can be replaced with multiple memory devices that each has an address range that is a subset of the logical memory address map. When one of the multiple memory devices is addressed in the logical memory address map, a corresponding physical address is generated from the logical address. The physical address is used to generate a chip select signal for that particular memory device.